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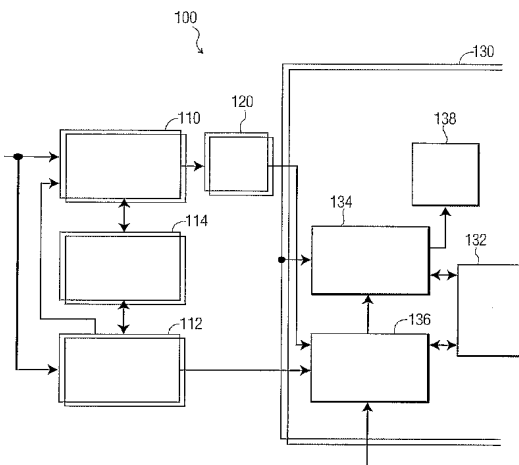
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(54) Title: IMAGE MOTION COMPENSATION ARRANGEMENT AND METHOD THEREFOR



(57) Abstract: Example embodiments are directed to a video image display system that includes a motion estimation circuit (112), a front-end motion compensation circuit (110), and a video signal conversion circuit (134, 136). The motion estimation circuit generates motion vectors as a function of an incoming video signal, the front-end motion compensation circuit receives and processes the incoming video signal as a function of the motion vectors for general video display, and the video signal conversion circuit receives the processed video signal from the front-end motion compensation circuit and generates a display signal for a specific video display as a function of both the processed video signal and the motion vectors. In one implementation, a scaler (120) is implemented to scale the video signal for a particular display type, with components of the motion compensation being implemented for use with the scaler.

WO 2005/073945 A1

**Declarations under Rule 4.17:**

- as to applicant's entitlement to apply for and be granted a patent (Rule 4.17(ii)) for the following designations AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SM, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, UZ, VC, VN, YU, ZA, ZM, ZW, ARIPO patent (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT,

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- as to the applicant's entitlement to claim the priority of the earlier application (Rule 4.17(iii)) for all designations

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